

FIG. 1

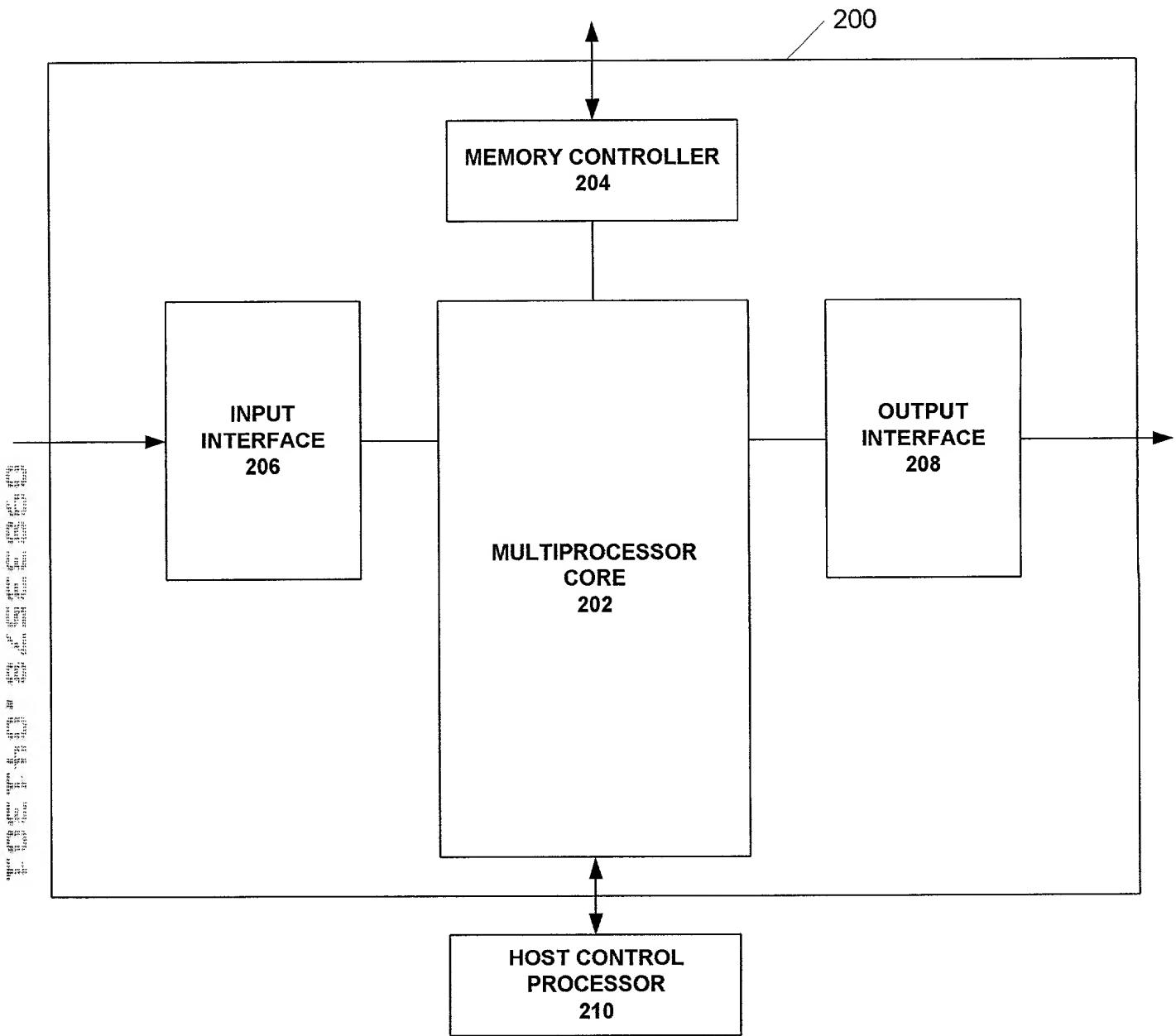


FIG. 2

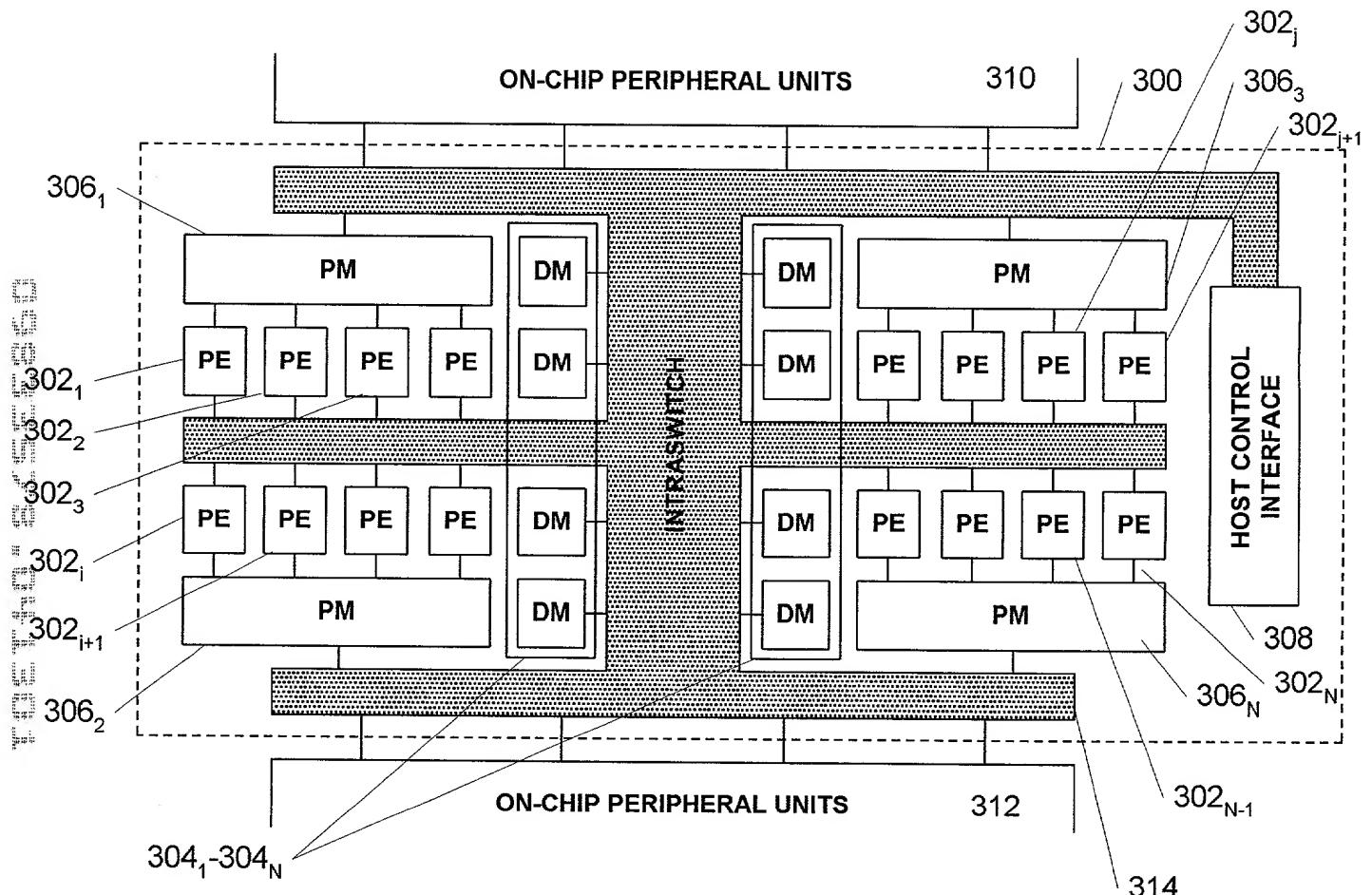


FIG. 3

PM: PROGRAM MEMORY
DM: DATA MEMORY
PE: PROCESSING ELEMENT

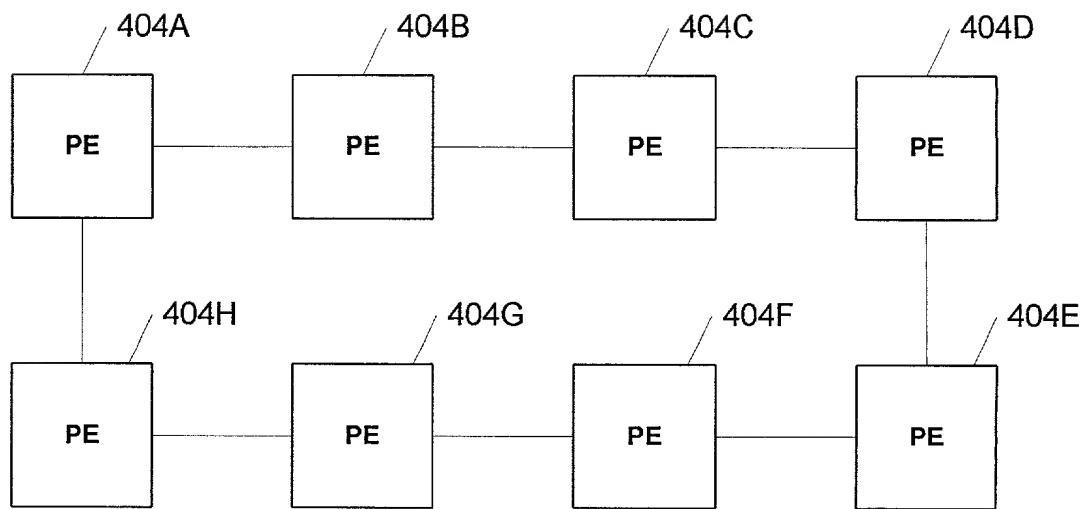


FIG. 4A

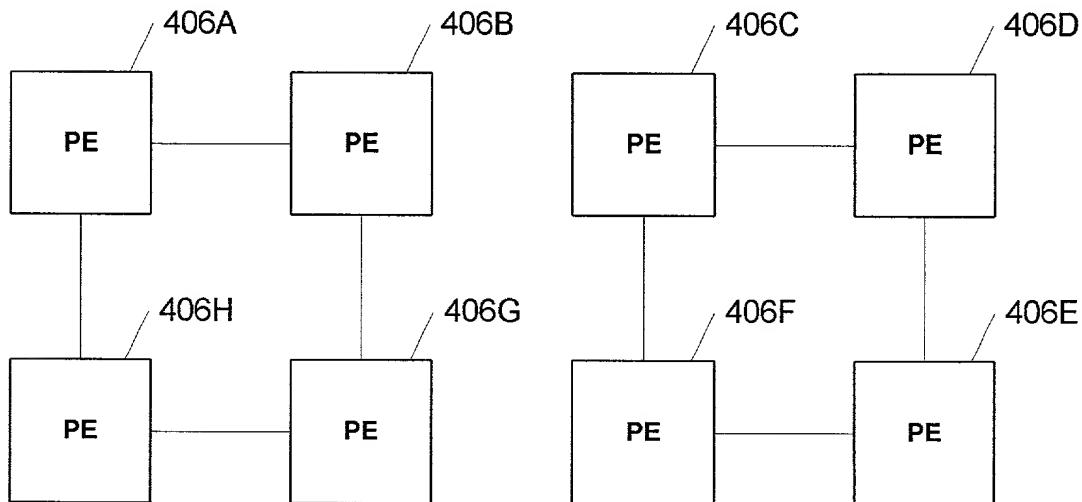


FIG. 4B

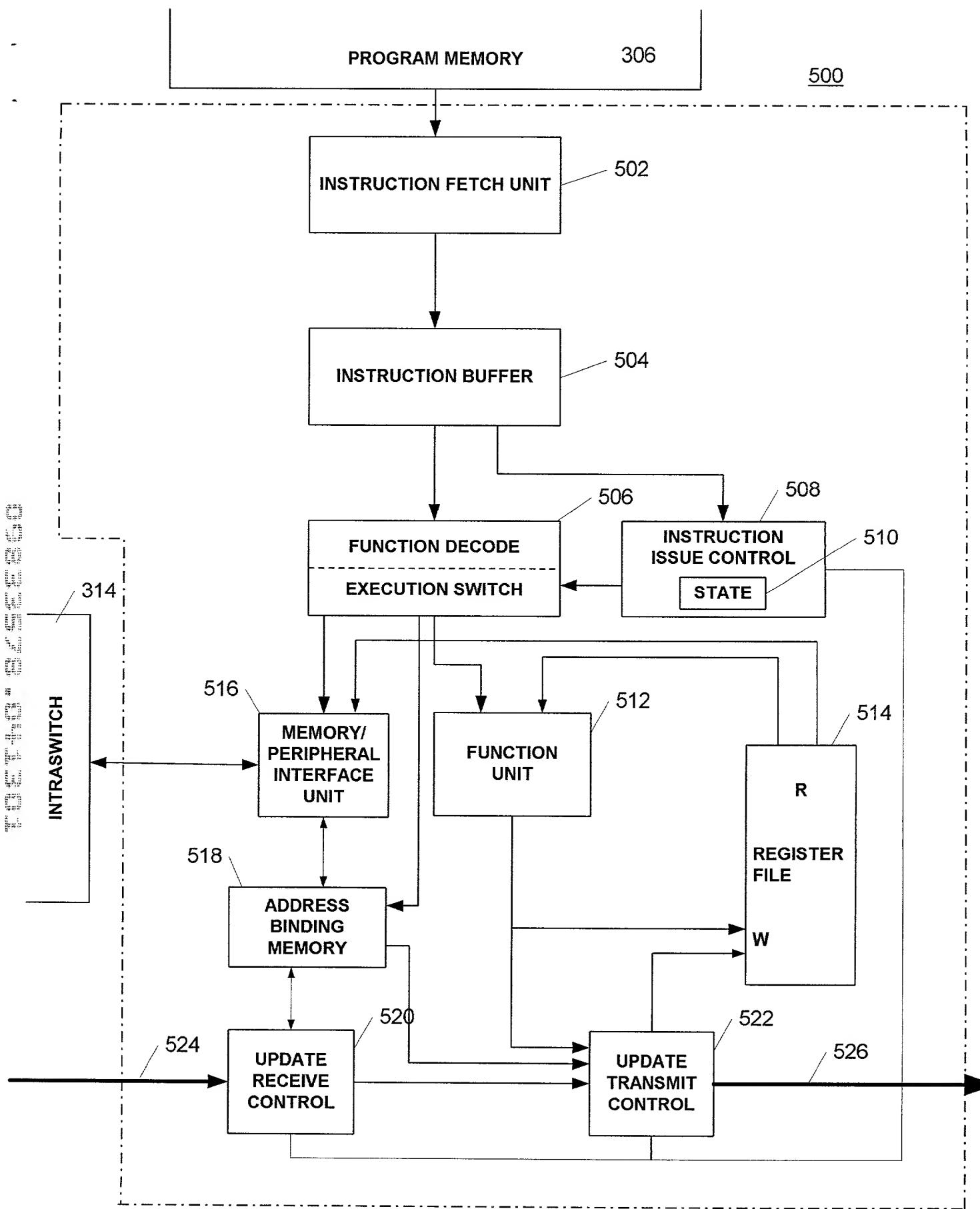


FIG. 5

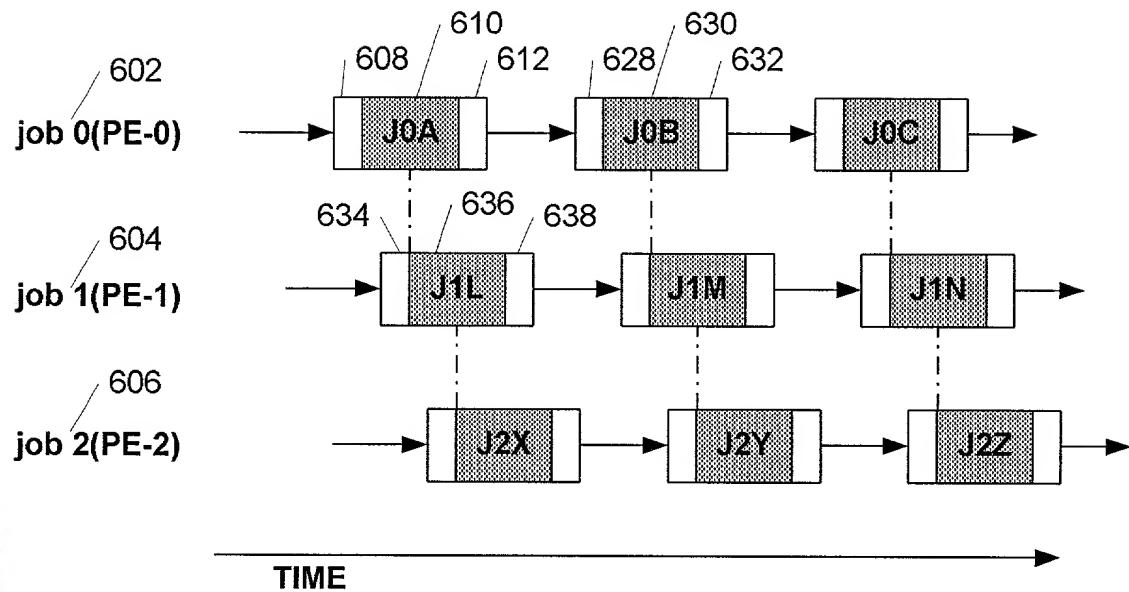
600

FIG. 6

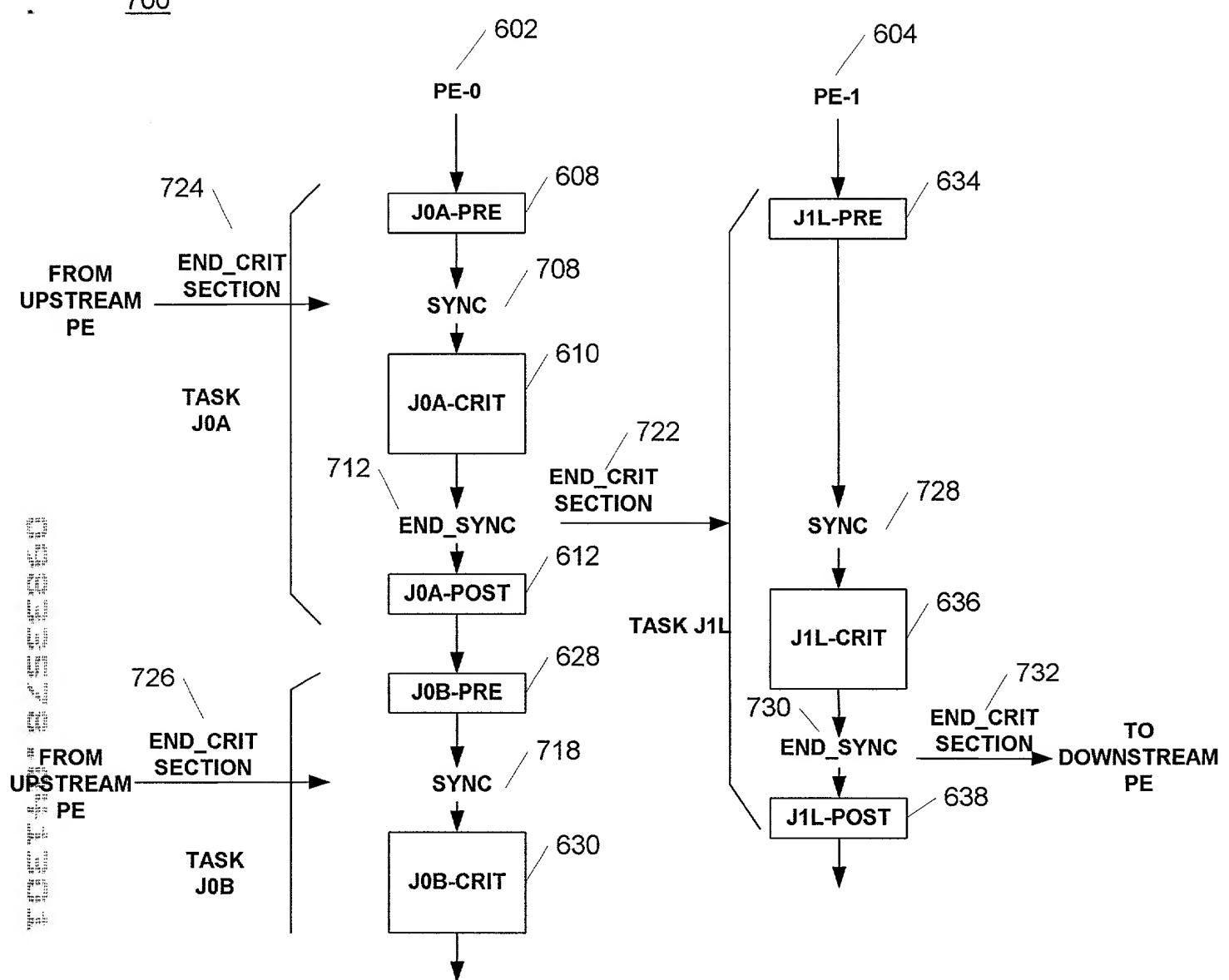


FIG. 7

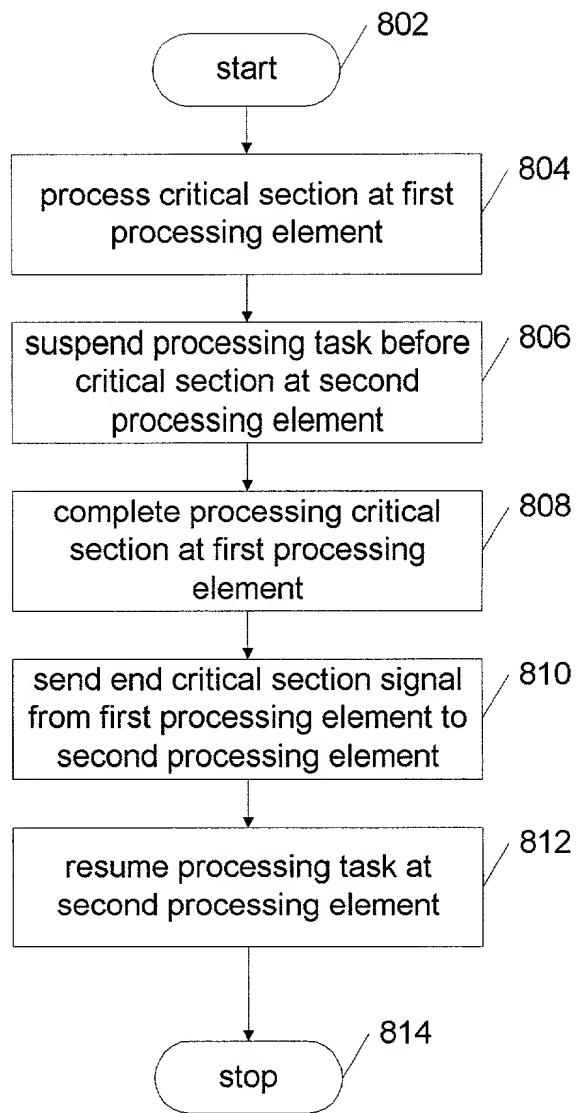
800

FIG. 8

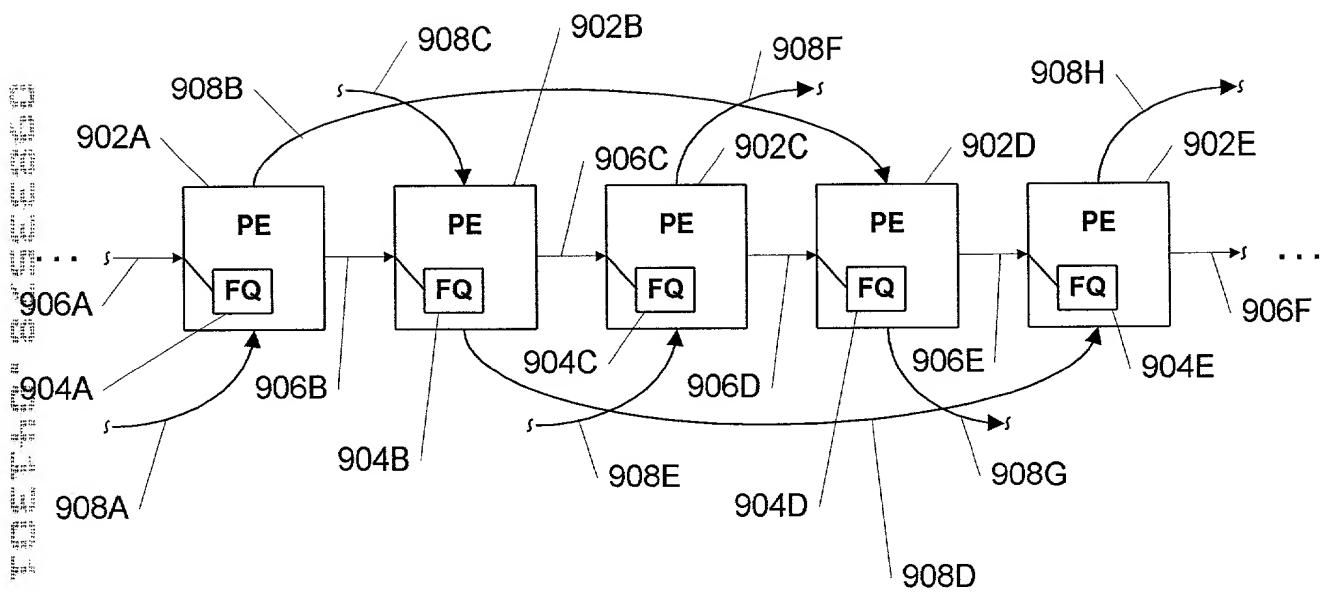
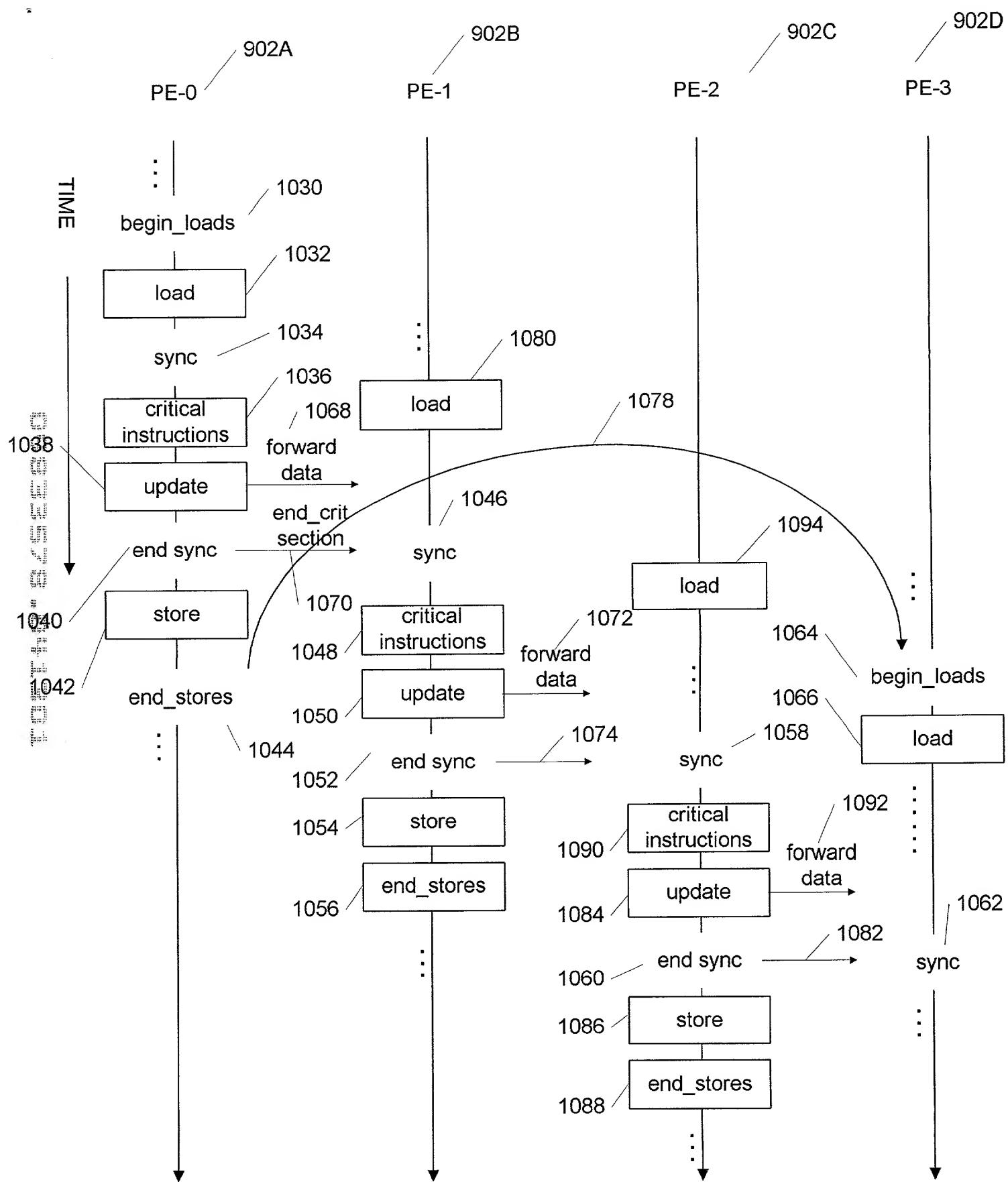
900

FIG. 9

FIG. 10

1000

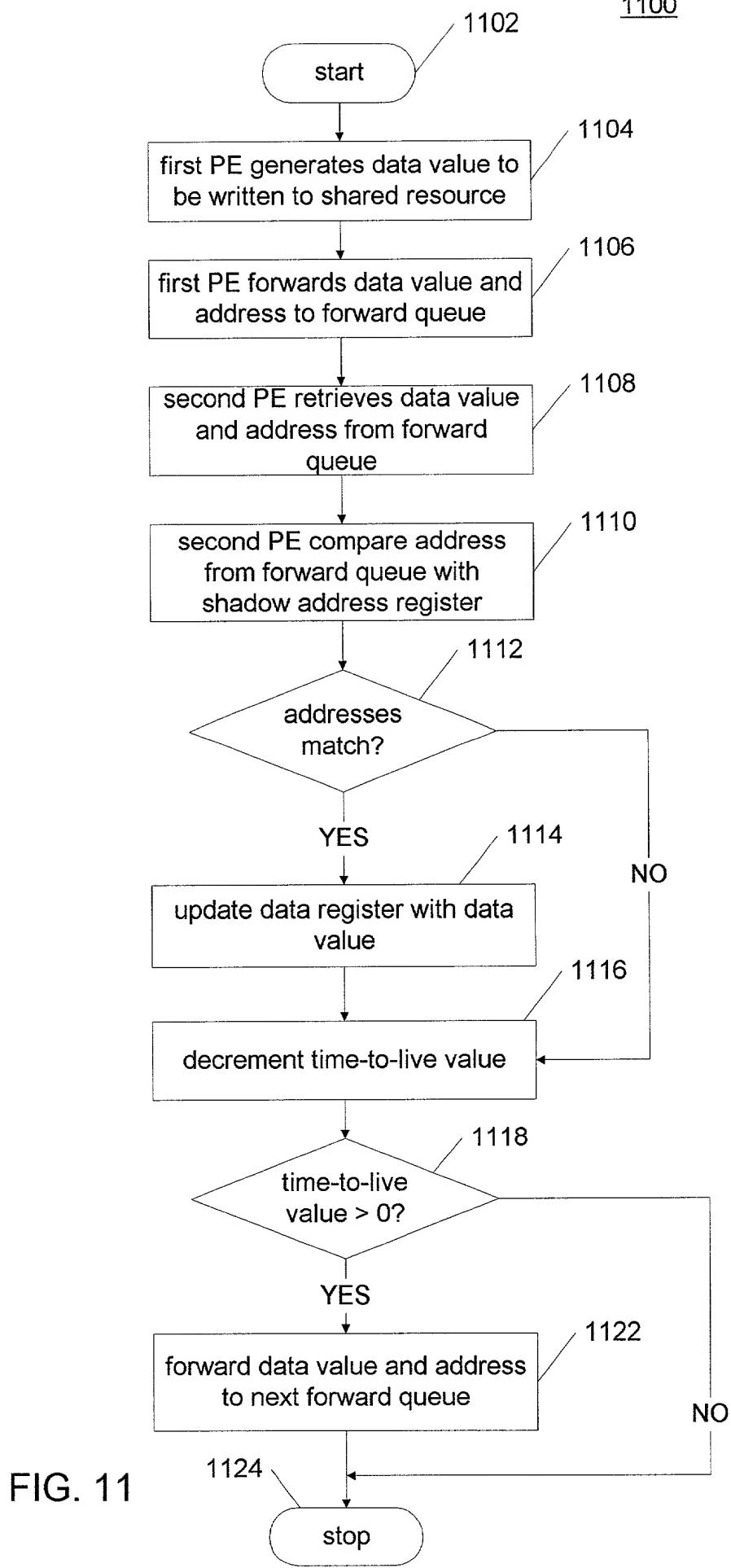


FIG. 11

1200

DATA	ADDRESS	REGISTER #	TTL	LAST UPDATE
D1	A1	R1	3	F
D2	A2	R2	2	F
D3	A3	R3	4	F
D4	A4	R4	1	T

FIG. 12

1300

opcode	source 0	source 1	destination	update	sync	endstores	beginloads

FIG. 13